Sheet 1 of 6 FURM PT01449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. (REV. 8-83) PATENT AND TRADEMARK OFFICE HARI.006USG 08/931,133 APPLICANT INFORMATION DISCLOSURE STATEMENT Eliyahou Harari et al. 👵 (Use several sheets if necessary) FILED GROUP 2785 September 16, 1997 U. S. PATENT DOCUMENTS *EXAMINER SUB FILING DOCUMENT NUMBER DATE NAME CLASS INITIAL CLASS DATE A1 1978 Das **A2** 2 9 0 2 4 1981 Schrenk A3 4 6 1 6 3 1 1986 Sato 7 1 8 1 Baglee et al. **A4** 4 0 4 1988 4 7 8 5 4 2 5 1988 Lavelle **A5** 0 1989 Iijima 8 0 O 5 2 **A6** 4 2 3 Iijima 8 R 7 4 1989 **A7** 4 8A 9 9 2 Δ n 1990 Iijima 4 4 0 5 3 9 9 n Kreifels et al. Α9 5 1991 A10 5 0 7 0 4 7 4 1991 Tuma et al. A11 9 5 3 4 4 1992 Harari FOREIGN PATENT DOCUMENTS SUB TRANS.? DOCUMENT NUMBER DATE CLASS COUNTRY CLASS (YES/NO 2 1983 В1 58 1 5 7 9 4 Yes Japan 5 **B2** 58 2 1 7 9 5 1983 Yes Japan В3 59 1 6 2 6 9 5 1984 Japan Yes 60 2 0 1985 Yes 1 2 9 0 Japan 62 1987 Japan Yes OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.) Cl Lucero et al., "A 16 kbit Smart 5 V-only EEPROM with Redundancy," IEEE Journal of Solid-State Circuits, vol. SC-18, no. 5, pps. 539-543 (October 1983) C2 Torelli et al., "An improved method for programming a word-erasable EEPROM," Alta Frequenza, vol. 52, no. 6, pps. 487-494 (Nov.-Dec. 1983)

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pps. 1-24 (May 1988)

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C3 Data Sheet: "27F256 256K(32K x 8) CMOS Flash Memory," Intel Corporation,

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	A14	4	6	4	2	7	5	9		1987	Foster							
•	A15	4	4	5	6	9	7	1		1984	Fukuda et	al.						
	A16	4	2	9	5	2	0	5		1981	Kunstadt							
	A17	4	6	1	7	6	2	4		1986	Goodman							
	A18	4	3	5	4	2	5	3		1982	Naden							
	A19	4	4	2	2	1	6	1		1983	Kressel et	al.						
×	A20	4	4	5	0	5	5	9		1984	Bond et al	•						
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FORM PT01449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. (REV. 8-83) PATENT AND TRADEMARK OFFICE HARI.006USG 08/931.133 APPLICANT INFORMATION DISCLOSURE STATEMENT Eliyahou Harari et al. (Use several sheets if necessary) FILED GROUP September 16, 1997 2785 U. S. PATENT DOCUMENTS EXAMINER SUB FILING CLASS DOCUMENT NUMBER DATE NAME INITIAL CLASS DATE A23 9 8 6 2 6 1990 Wayama et al. Hosotani et al. A24 9 5 5 3 5 1990 A25 4 3 8 0 0 6 6 1983 Spencer et al. 3 5 Haeusele A26 4 4 6 4 0 1984 A27 4 3 5 5 3 7 6 1982 Gould 3 A28 3 6 3 1 7 5 1972 Harper A29 9 8 4 6 Martinez 4 4 1 1985 2 3 3 A30 9 4 1 1990 Robinson et al. 4 A31 4 4 7 9 2 1 4 1984 Ryan A32 4 9 5 3 1 2 2 1990 Williams **A33** 9 2 0 5 1 8 1990 Nakamura et al. FOREIGN PATENT DOCUMENTS SUB TRANS.? DOCUMENT NUMBER DATE COUNTRY CLASS CLASS (YES/NO B11 0 0 8 6 8 8 6 1983 European Patent Appln. B12 | 2 1 3 6 9 9 2 1984 Α UK Patent Appln. B13 0 2 4 3 5 0 3 В1 1987 European Patent Appln. B14 3 0 0 2 6 4 В1 1989 European Patent Appln. **B15** 0 5 5 7 7 2 3 1987 AU Patent Abridgement OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.) C7 Miller, "Semidisk Disk Emulator," Interface Age, p. 102, November, 1982 C8 Clewitt, "Bubble Memories as a Floppy Disk Replacement," 1978 MIDCON Technical Papers, vol. 2, pp. 1-7, Dec. 1978 C9 Hancock, "Architecting a CCD Replacement for the IBM 2305 Fixed Head Disk Drive," Digest of Papers, Eighteenth IEEE Computer Society International Conference, pp. 182-184, 1979 C10 Wilson, "1-Mbit flash memories seek their role in system design," Computer Design, vol. 28, no. 5, pps. 30-32 (March 1989) EXAMINER DATE CONSIDERED

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	A38	5	1	6	3	0	2	1		1992	Mehrotra e				
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	A40	4	7	9	4	5	6	8		1988	Lim et al.	· ·			
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	A47	4	2	5	0	5	7	0		1981	Tsang et a	1.			
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U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. FORM PT01449 (REV. 8-83) PATENT AND TRADEMARK OFFICE HARI.006USG 08/931,133 APPLICANT INFORMATION DISCLOSURE STATEMENT Eliyahou Harari et al. (Use several sheets if necessary) FILED GROUP September 16, 1997 U. S. PATENT DOCUMENTS EXAMINER SUB FILING DOCUMENT NUMBER DATE NAME CLASS INITIAL CLASS DATE A51 Hoffman. A52 Kroll et al. Nakamura et al. **A53** Δ A54 Tayler et al. A55 Furuya et al. **A56** Terada et al. A57 Fukuda et al A58 Berger et al. A59 Beardsley et al. Bastian et al. A60 A61 Harari A62 Mehrotra et al. Harari et al. A63 q Harari et al. A64 R A65 Fujita et al. A66 Norman et al. A67 Hewitt et al. A68 Lambrache et al. A69 Tanaka et al. FOREIGN PATENT DOCUMENTS SUB TRANS.? DOCUMENT NUMBER DATE COUNTRY CLASS CLASS (YES/NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.) Cll Bleiker et al., "A Four-State EEPROM Using Floating-Gate Memory Cells," IEEE Journal of Solid-State Circuits, SC-22 (1987) June, No. 3, New York, NY, USA C12 Intel Corporation, 27F256, 256K (32K x 8) CMOS FLASH MEMORY, May 1988, pp. 1-21 EXAMINER DATE CONSIDERED * EXAMINER: Initial if citation considered, whether or not citation is in conformance with

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